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| APPLICATION NO.   | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|---|-------------|----------------------|---------------------|------------------|
| 10/580,807  | 05/25/2006  | Angela Renee Burnett | PU030306            | 8850             |
| 7590  | 03/18/2009  |                      | EXAMINER            |                  |
| Joseph J Laks<br>Thomson Licensing Inc<br>Patent Operations<br>O O Box 5312<br>Princeton, NJ 08543-5312 |             |                      |                     | LEWIS, DAVID LEE |
| ART UNIT  |             | PAPER NUMBER         |                     |                  |
| 2629  |             |                      |                     |                  |
|   |             | MAIL DATE            | DELIVERY MODE       |                  |
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**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

|                              |                        |                       |  |
|------------------------------|------------------------|-----------------------|--|
| <b>Office Action Summary</b> | <b>Application No.</b> | <b>Applicant(s)</b>   |  |
|                              | 10/580,807             | BURNETT, ANGELA RENEE |  |
|                              | <b>Examiner</b>        | <b>Art Unit</b>       |  |
|                              | DAVID L. LEWIS         | 2629                  |  |

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

1) Responsive to communication(s) filed on 25 May 2006.

2a) This action is FINAL.                    2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

4) Claim(s) 1-14 is/are pending in the application.

4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.

5) Claim(s) \_\_\_\_\_ is/are allowed.

6) Claim(s) 1-14 is/are rejected.

7) Claim(s) \_\_\_\_\_ is/are objected to.

8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on 25 May 2006 is/are: a) accepted or b) objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All    b) Some \* c) None of:

1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

|  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)            | 4) <input type="checkbox"/> Interview Summary (PTO-413)           |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)   | Paper No(s)/Mail Date. _____ .                                    |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date <u>5/25/2006</u> .   | 6) <input type="checkbox"/> Other: _____ .                        |

## DETAILED ACTION

### ***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

1. **Claims 1-14 are rejected under 35 U.S.C. 102(e) as being anticipated by Mears et al. (6704061).**

**As in claim 1, Mears et al. teaches of a video display, figure 3 item 300, column 2 lines 47-56,**

comprising: a controller responsive to a first signal by providing power to a first set of circuits of said display, **figure 3 item 311;**

said controller responsive to a second signal by removing power from a second set of circuits of said video display; **column 3 lines 7-45, column 4 lines 1-35, column 5 lines 1-15**

a timer defining a time interval between an instance of said second signal and an instance of said first signal, **figure 3 item 312/314;**

wherein said first set of circuits is different from said second said of circuits during said time interval, **column 3 lines 7-45, column 4 lines 1-35, column 5 lines 1-15 .**

**As in claim 2, Mears et al. teaches of a** wherein, during said time interval, said first set of circuits excludes a lamp power circuit and said second set of circuits includes said lamp power circuit, column 3 lines 7-45, column 4 lines 1-35, column 5 lines 1-15.

**As in claim 3, Mears et al. teaches of a** wherein said lamp power circuit is coupled to an image lighting lamp, figure 3 item 302.

**As in claim 4, Mears et al. teaches of a** wherein said image lighting lamp is a mercury vapor lamp, figure 3 item 302.

**As in claim 5, Mears et al. teaches of a**, outside of said time interval said first set of circuits and said second set of circuits are substantially the same, column 3 lines 7-45, column 4 lines 1-35, column 5 lines 1-15.

**As in claim 6, Mears et al. teaches of a** video display, **figure 3 item 300**,

comprising: a controller capable of responding to a first signal by energizing a first set of circuits of said display, **figure 3 item 311**;

said controller capable of responding to a second signal by de-energizing a second set of circuits of said display, **column 3 lines 7-45, column 4 lines 1-35, column 5 lines 1-15**;

a timer defining a time interval between an instance of said second signal and an instance of said first signal, **figure 3 item 312/314**;

wherein said controller selects members of said first set of circuits based upon a condition of said timer, **column 3 lines 7-45, column 4 lines 1-35, column 5 lines 1-15.**

**As in claim 7, Mears et al. teaches of a** wherein said first signal is a power on signal and said second signal is a power off signal, column 3 lines 7-45, column 4 lines 1-35, column 5 lines 1-15.

**As in claim 8, Mears et al. teaches of a** wherein said timer comprises a lamp cool down timer for a high intensity discharge lamp, figure 3 item 312/314.

**As in claim 9, Mears et al. teaches of a** wherein both said first set of circuits and said second set of circuits include circuits selected from the group comprising, tuners, speakers and amplifiers, and audio video input circuits, column 3 lines 7-45, column 4 lines 1-35, column 5 lines 1-15.

**As in claim 10, Mears et al. teaches of a** wherein said first signal and second signals are provided by a user operable control device, column 3 lines 7-45, column 4 lines 1-35, column 5 lines 1-15.

**As in claim 11, Mears et al. teaches of a** a video display system, **figure 3 item 300,**

comprising: an image lighting lamp, **figure 3 item 302 ;**

a control circuit for applying and removing power for said image lighting lamp, **figure 3 item 311;**

said control circuit including a timer, **figure 3 item 312/314;**

said timer commencing a time interval when said control circuit removes power from said image lighting lamp, **column 3 lines 7-45, column 4 lines 1-35, column 5 lines 1-15;**

said control circuit maintaining said image lighting lamp de-energized during said time

interval, **column 3 lines 7-45, column 4 lines 1-35, column 5 lines 1-15;**

wherein said control circuit is capable of applying power to circuits of said video display apparatus, other than said image lighting lamp, during said time interval, **column 3 lines 7-45, column 4 lines 1-35, column 5 lines 1-15.**

**As in claim 12, Mears et al. teaches of a method for applying power to circuits of a video display, figures 1-3,**

the method comprising the steps of: applying power to a first set of said circuits in response to a first signal, **column 3 lines 7-45, column 4 lines 1-35, column 5 lines 1-15;**

removing power from a second set of said circuits in response to a second signal, **column 3 lines 7-45, column 4 lines 1-35, column 5 lines 1-15;**

defining a time interval between an instance of said second signal and an instance of said first signal and during which said first set of circuits is different from said second said of circuits and outside of which, said first set of circuits is the same as said second set of circuits, **column 3 lines 7-45, column 4 lines 1-35, column 5 lines 1-15.**

**As in claim 13, Mears et al. teaches of a method for applying power to circuits of a video display, figures 1-3,**

the method comprising the steps of: applying power to a first set of said circuits in response to a first signal, **column 3 lines 7-45, column 4 lines 1-35, column 5 lines 1-15;**

removing power from a second set of said circuits in response to a second signal, **column 3 lines 7-45, column 4 lines 1-35, column 5 lines 1-15;**

indicating a time interval between an instance of said second signal and an instance of said first signal, **figure 3 item T1, column 3 lines 7-45, column 4 lines 1-35, column 5 lines 1-15;**

determining members of said first set of circuits based upon said indication, **column 3 lines 7-45, column 4 lines 1-35, column 5 lines 1-15.**

**As in claim 14, Mears et al. teaches of** In a video display apparatus, a method for controlling an image lighting lamp circuit, **figures 1-3,**

the method comprising the steps of: energizing selected circuits of said video display apparatus in response to a power on control signal, **column 3 lines 7-45, column 4 lines 1-35, column 5 lines 1-15;**

de-energizing circuits of said video display in response to a power off control signal, **column 3 lines 7-45, column 4 lines 1-35, column 5 lines 1-15;**

utilizing said power off control signal to start a timer, **column 3 lines 7-45, column 4 lines 1-35, column 5 lines 1-15;**

providing an indication of a condition of said timer, **column 3 lines 7-45, column 4 lines 1-35, column 5 lines 1-15;**

wherein said selected circuits are selected based upon said indication, **column 3 lines 7-45, column 4 lines 1-35, column 5 lines 1-15.**

### Conclusion

2. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **David L. Lewis** whose telephone number is **(571) 272-7673**. The examiner can normally be reached on MT and THF from 8 to 5. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin Shalwala, can be reached on **(571) 272-7681**. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (571)-273-8300.
  
3. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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Examiner: David L. Lewis

***March 15, 2009***

***/David L Lewis/***

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